## SUB SYSTEMS: DUALS

					-												Model Designator								
		Power Supply		Power Down		Conversion	n Accuracy		Differential Linearity		Full Scale Error		Zero		Bipolar						Temperature			Starting	
MODEL		Requirements				Rate	or Linearity						Error		Zero Error		Voltage		Tristate	I/O	Range		ge		#
	# Bits	Min	+Icc	Iq uA	Range	MSPS	Lsb's		Lsb's		Lsb's		Lsb's		Lsb's		Reference		Output		-25	-40	-55	of	Price
		+Vcc	mA				+25C	Tmax	+25C	Tmax	+25C	Tmax	+25C	Tmax	+25C	Tmax	INT	EXT	Latches		85	85	125	Pins	/100's
AD9066	6	+5V	120		500mV	60	1		1/2	2							+2V		2 P6	J		٨		28	\$5.40
							1 1 2				_	2 /1 /2				4 (1 (2	+2 V	211	-		- 4	A			-
AD9058	8	+5V	154		+2V	50	1.3	1.4	0.65	0.8	2	3 /1/2			3	4 /1/2		+2V	NO	2 P8	J			44	\$33.50
AD9058	8	-5V	38				1	1 1/4	1/2	0.7											K		T		\$65.84
AD9059	8	+5V	107		1Vpp	60	2	2 1/2	2	2 1/2	15	20	4	6			+2.5V		2 P8			X		20	\$16.48
AD9281	8	+5V	58		0>Vdd	20	1		1/2		TBD		TBD				PRGM		P8						\$12.00
AD9201	10	+3V	58		0>Vdd	20	1		1/2		TBD		TBD				PRGM		P10						\$24.95
					0>2.5																				
AD7862	12	+5V	15	25	±2.5,±10	0.25	1	1	1	1	4	4	4	4	4	4	+2.5V		Yes	P12		A	S	28	\$12.95
AD7862	12										3	3	3	3	3	3			Yes	P12	1	В			\$16.85
AD7863	14	+5V	15	1	0>2.5	200	2	2	1	1	4	4	4	4	4	4	+2.5V			P14		Α		28	\$18.00
					±2.5,±10																				
AD7863	14						1	1														В			tbd
Hybrid, v	vith 2	AD9042	2, w/IN	PUT SI	INGAL C	CONDITIO	NING	, OUT	PUT I	BUFF	ERING	<b>,</b>													
AD10242	12	+5V	285		+/-0.5/1/2	41	NS		NS		1%FS	1.5%FS	NA		0.5%FS	1.5%FS	+2.5V		NO	P12		В	T	68	\$600.00
AD10242	12	-5V	55																						

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